

Fig.1

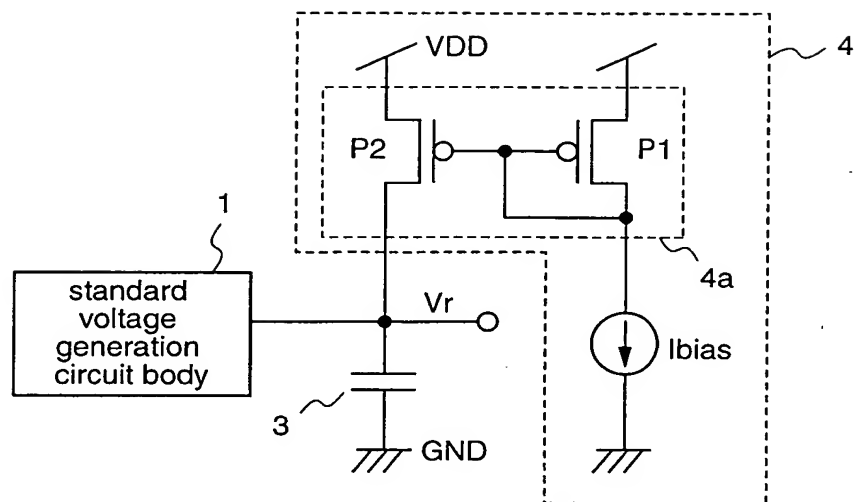


Fig.2

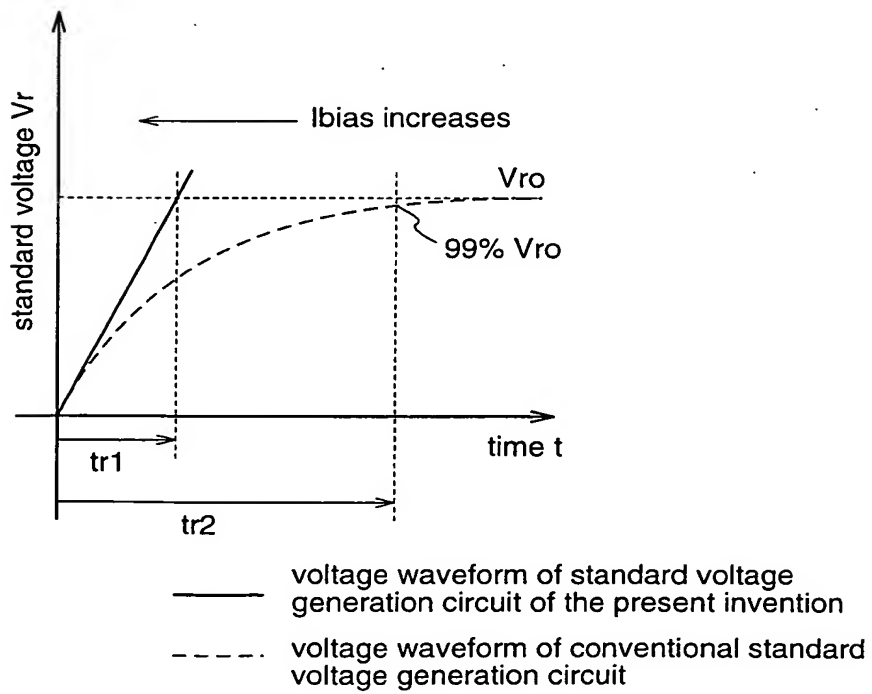


Fig.3

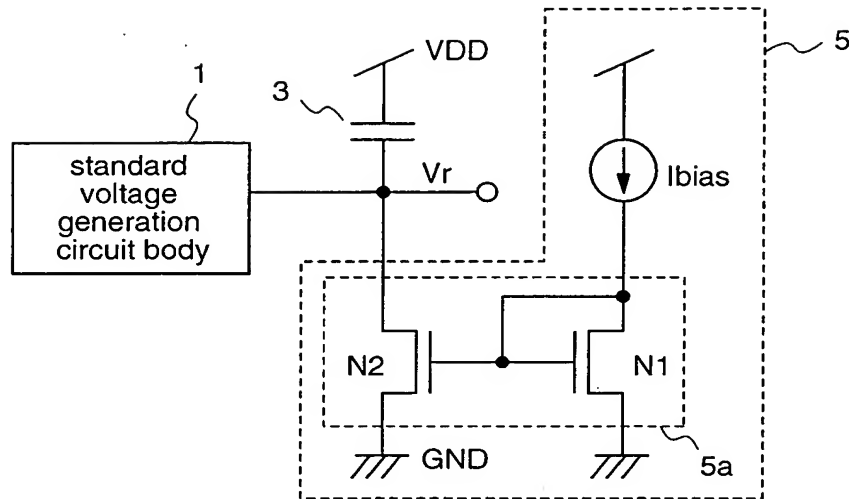
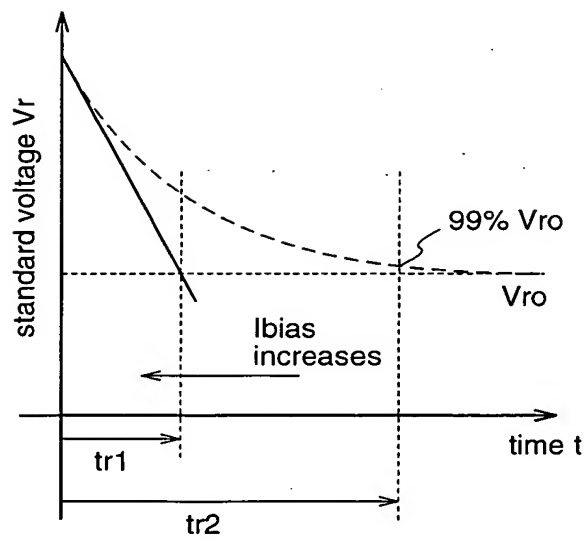


Fig.4



- voltage waveform of standard voltage generation circuit of the present invention
- - - - voltage waveform of conventional standard voltage generation circuit

Fig.5

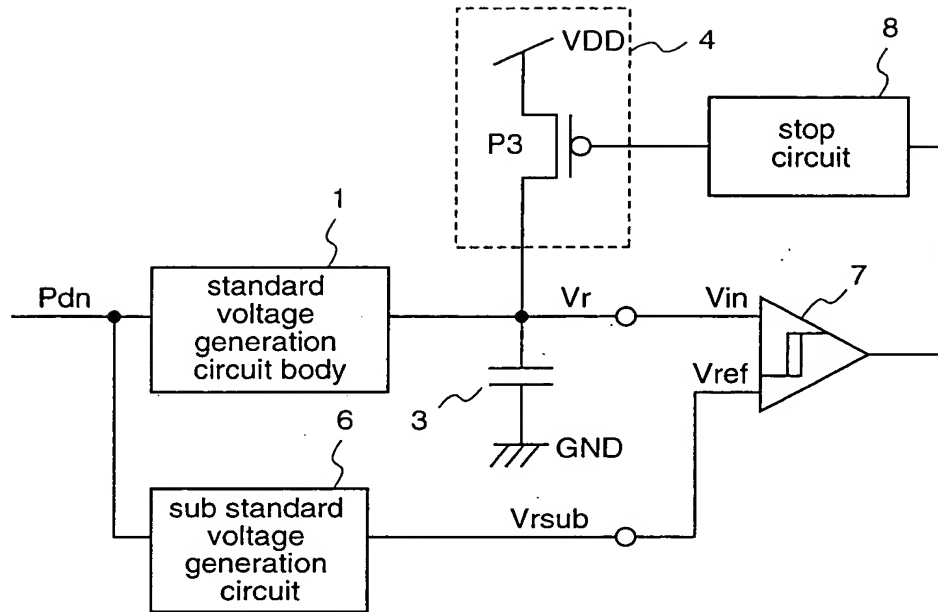


Fig.6

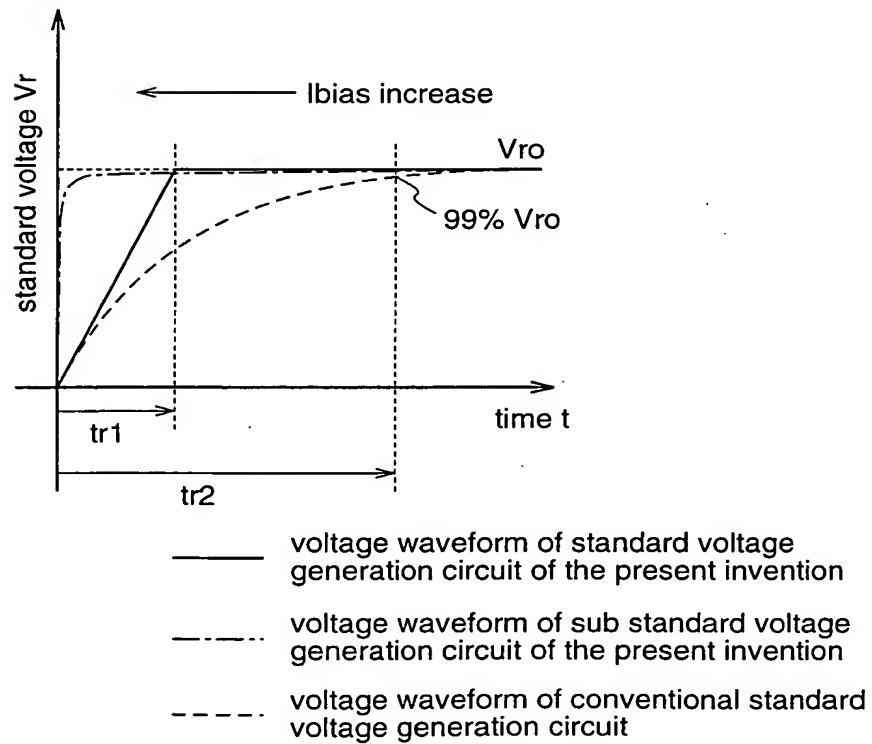


Fig.7

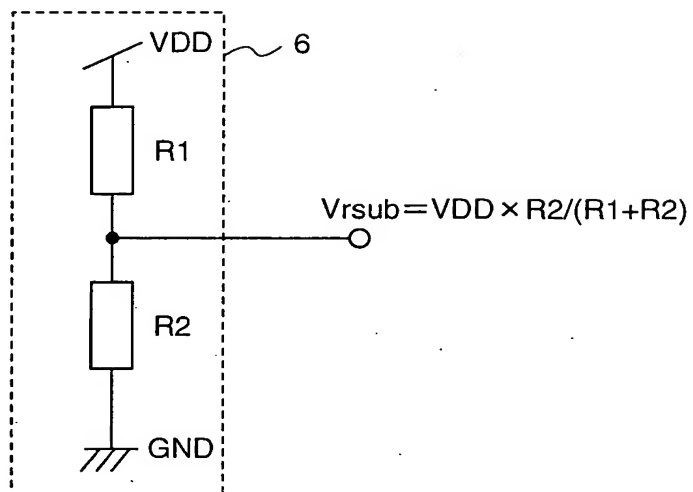


Fig.8

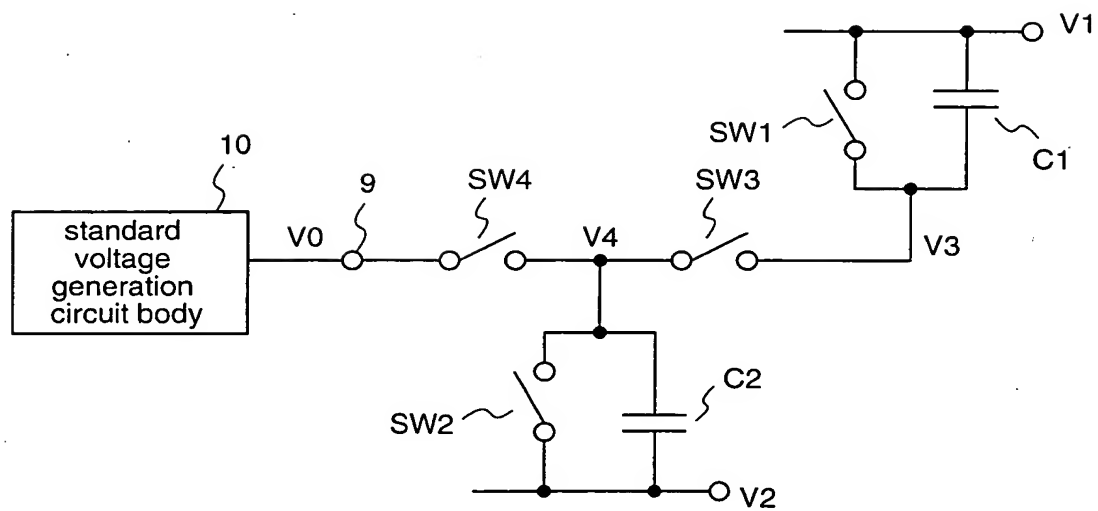


Fig.9

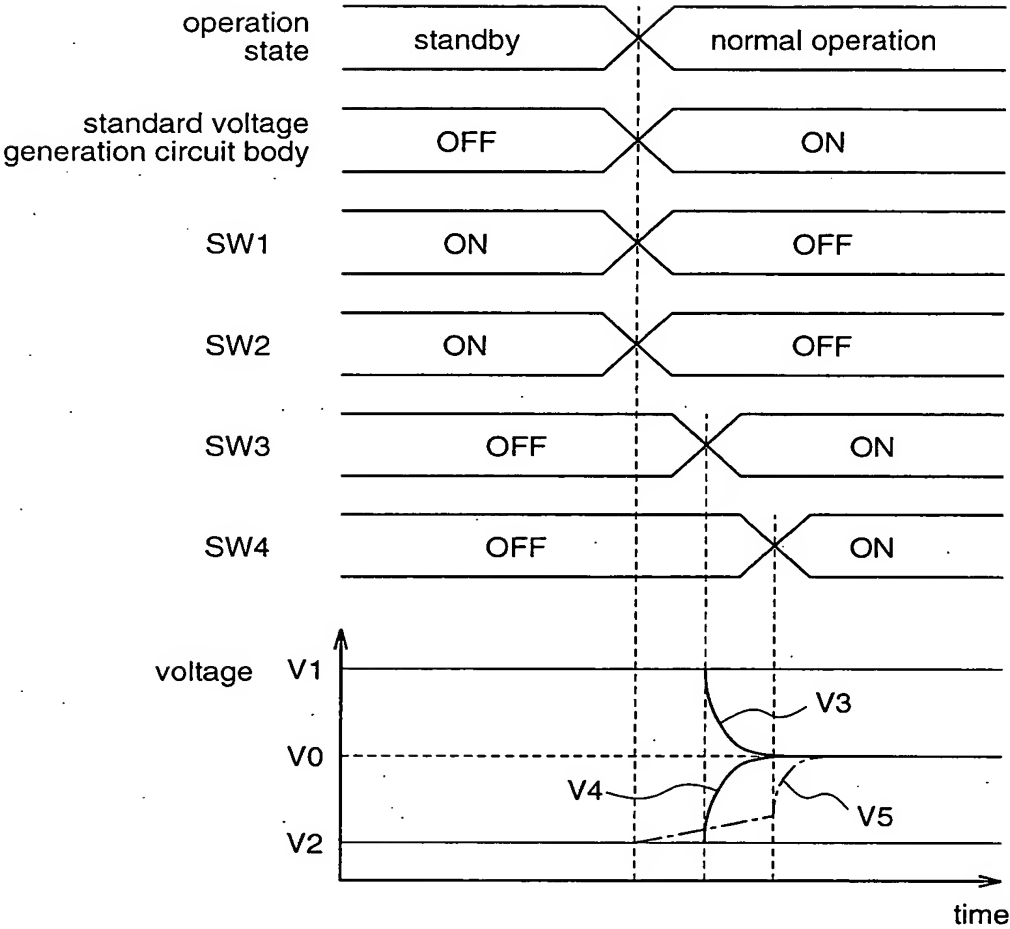


Fig.10

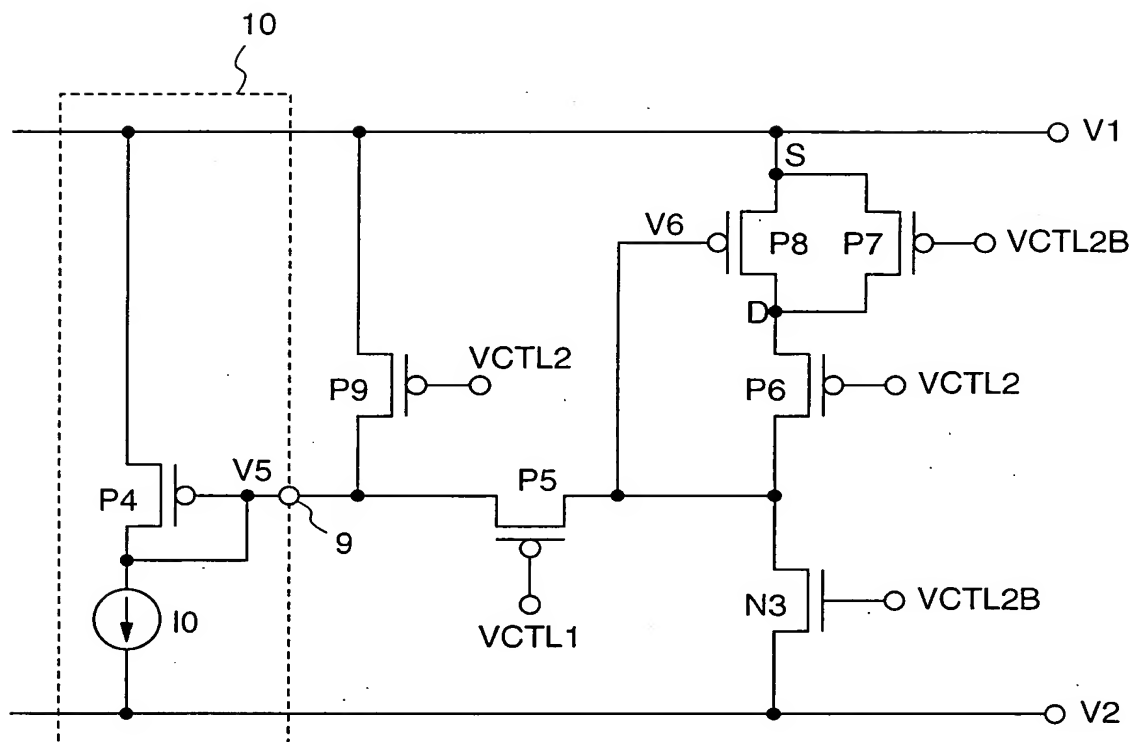


Fig.11

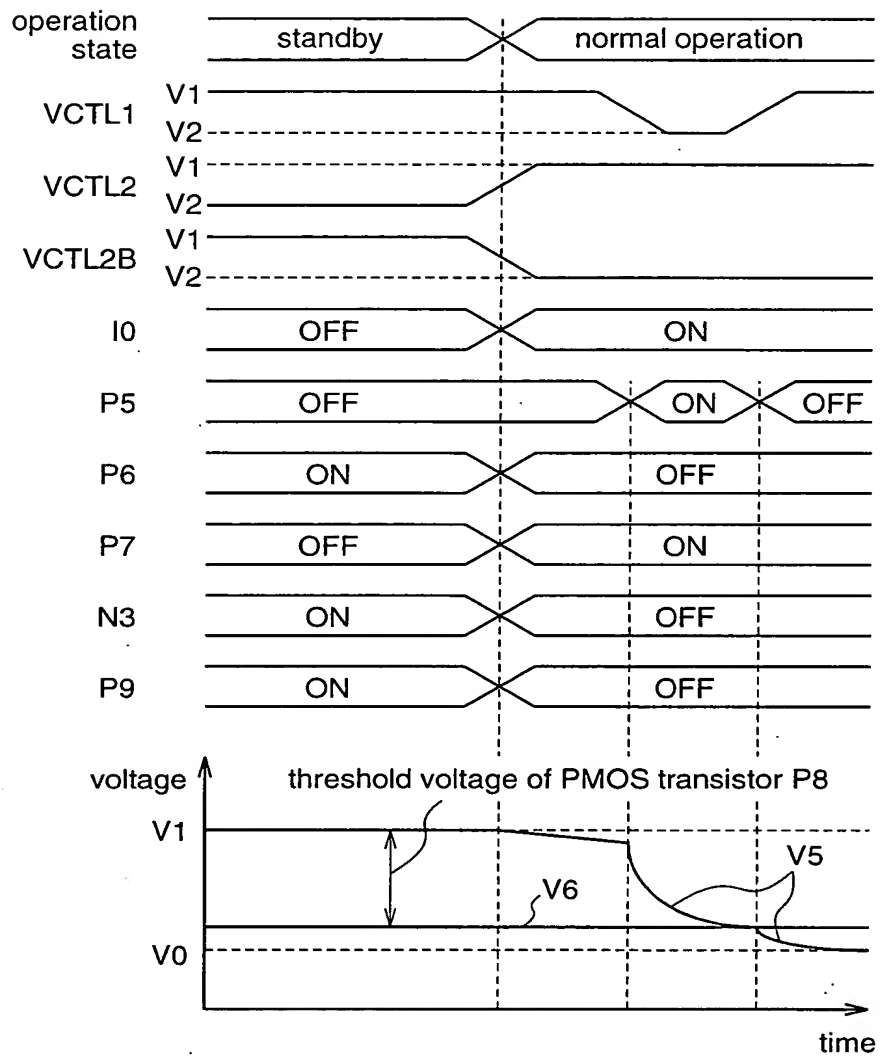


Fig.12

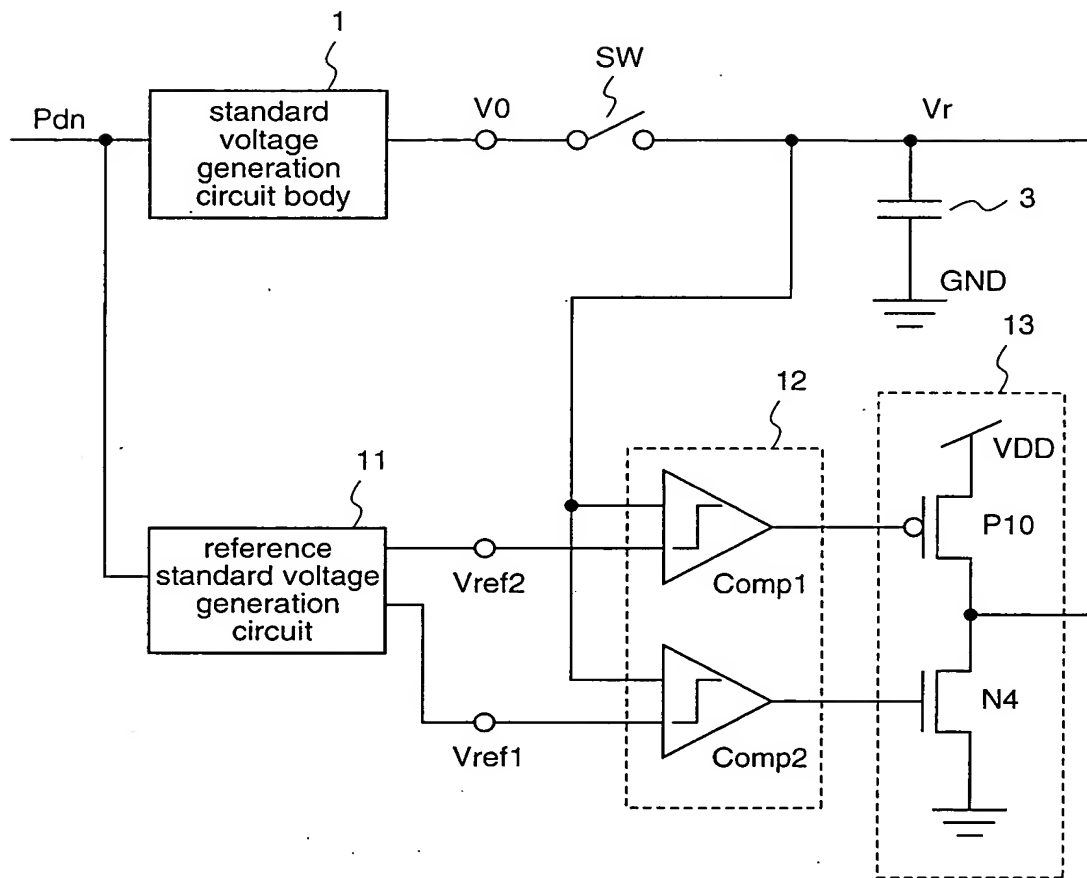


Fig.13

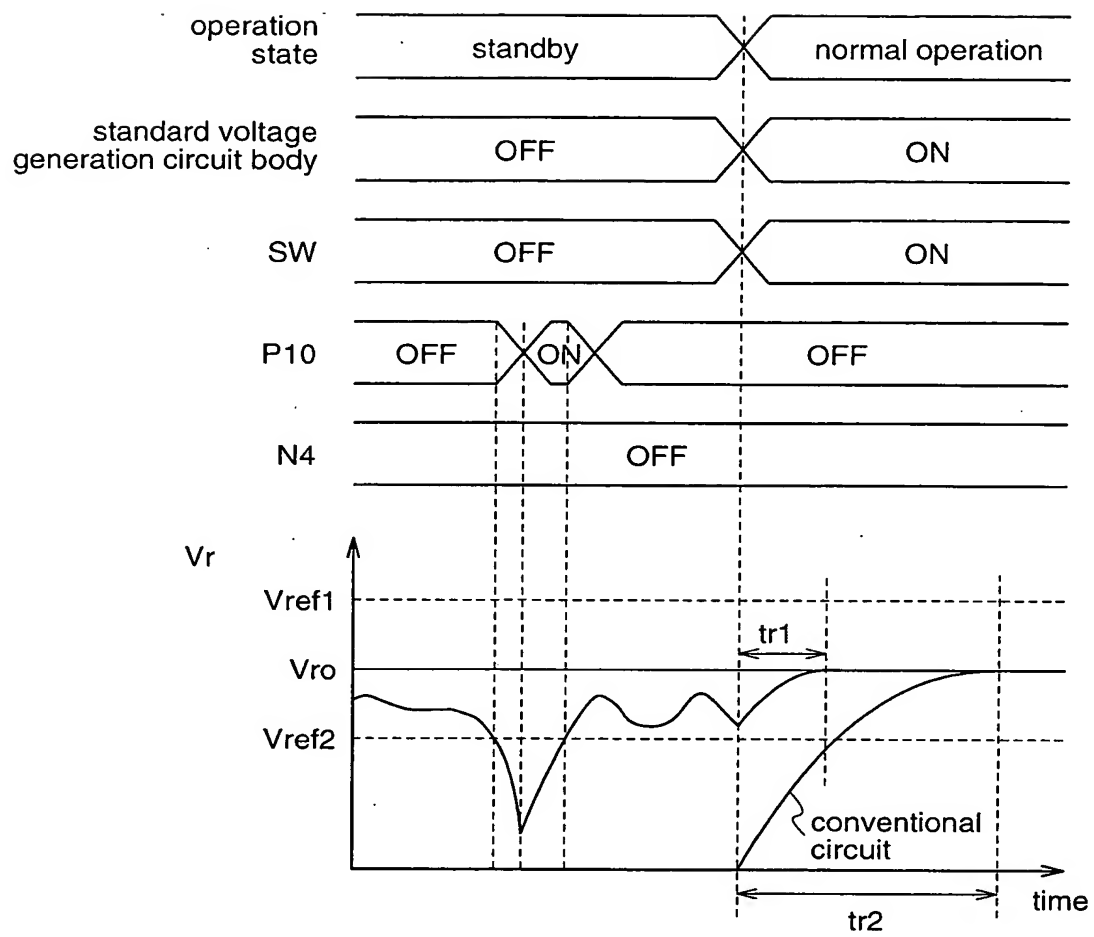
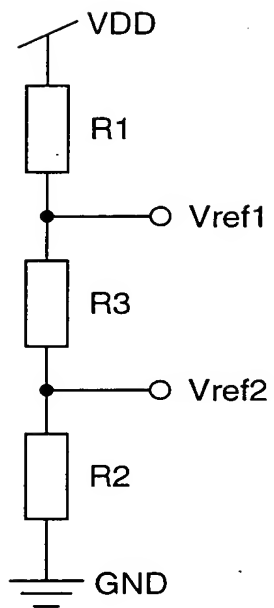


Fig.14



$$V_{ref1} = VDD \cdot (R2 + R3) / (R1 + R2 + R3)$$
$$V_{ref2} = VDD \cdot (R2) / (R1 + R2 + R3)$$

Fig.15

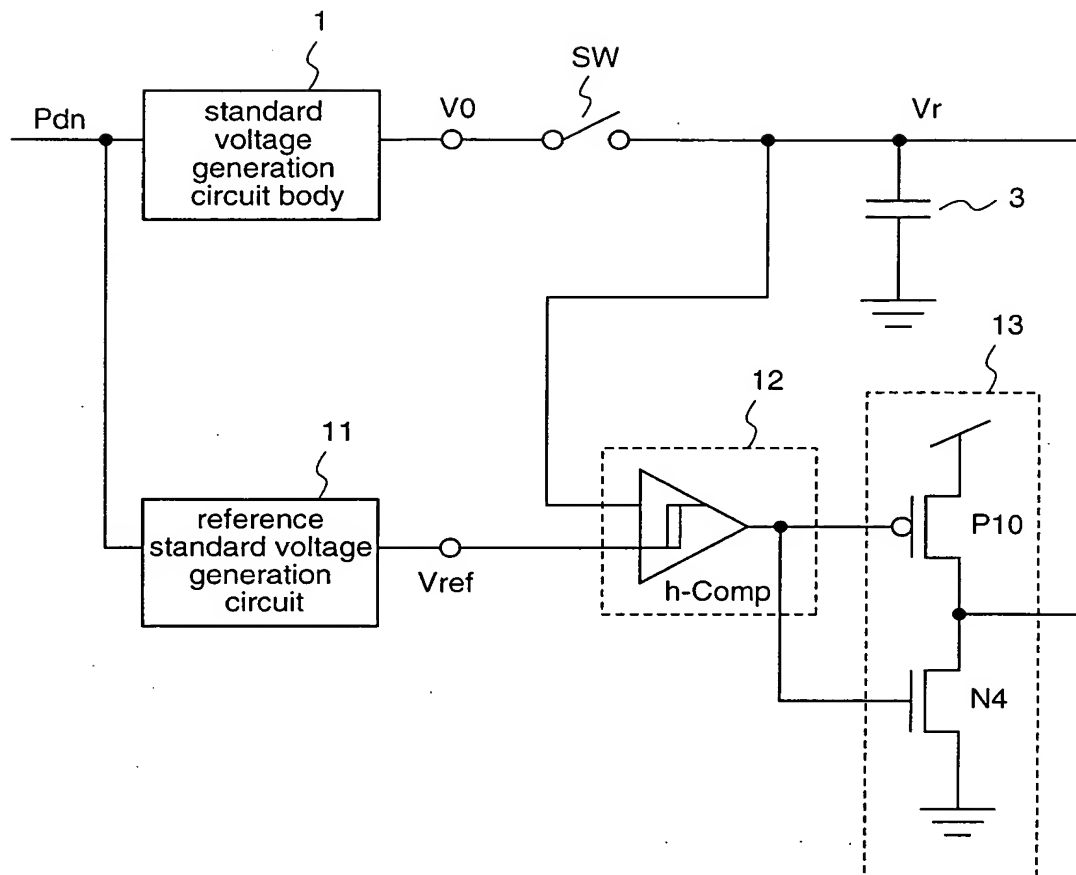


Fig.16

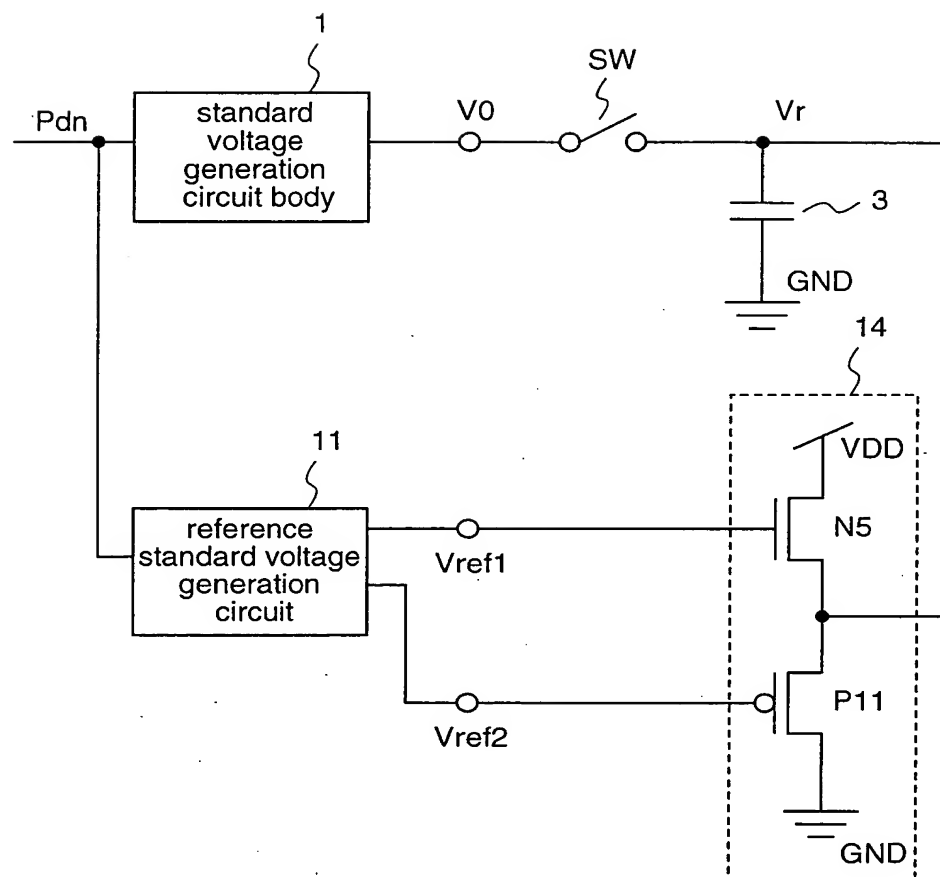


Fig.17

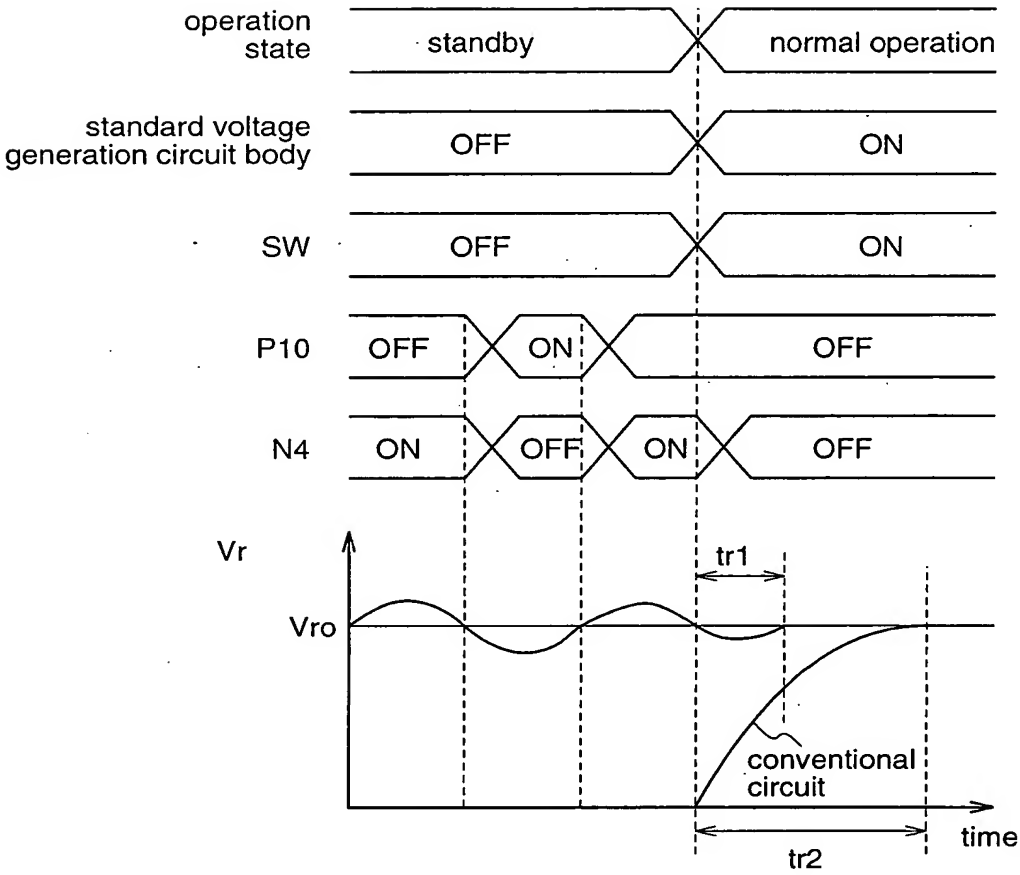


Fig.18

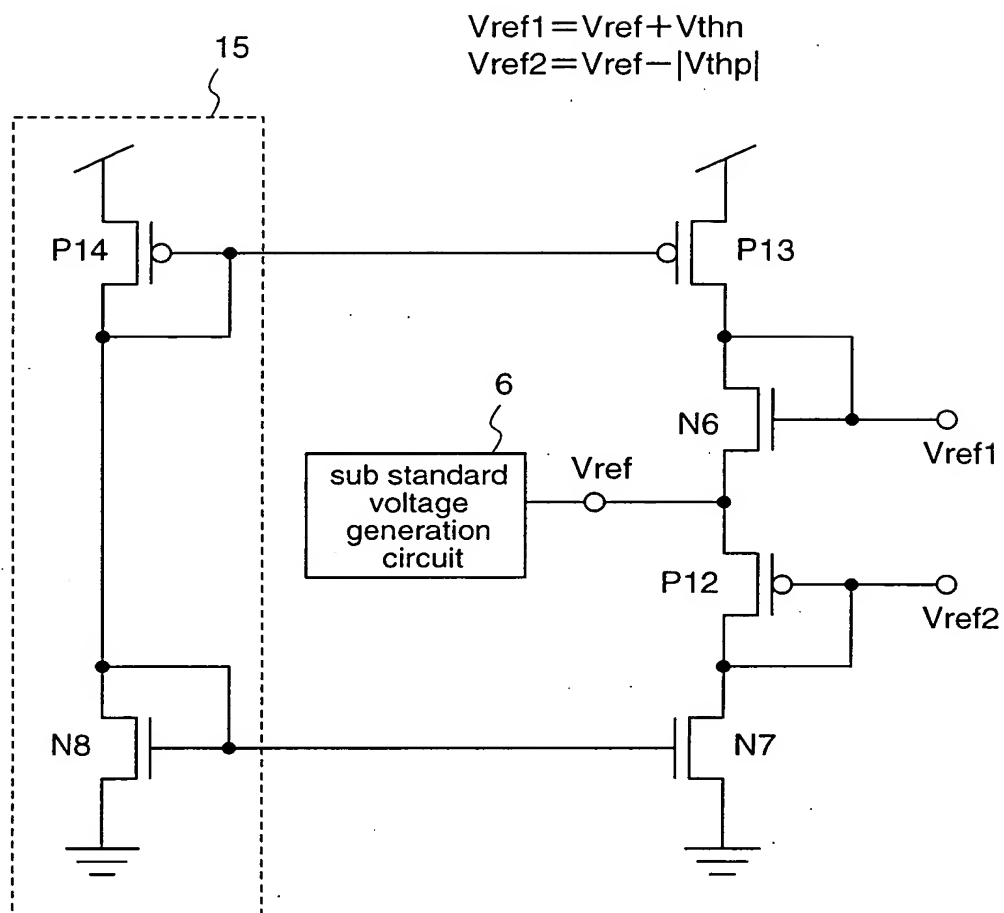


Fig.19 Prior Art

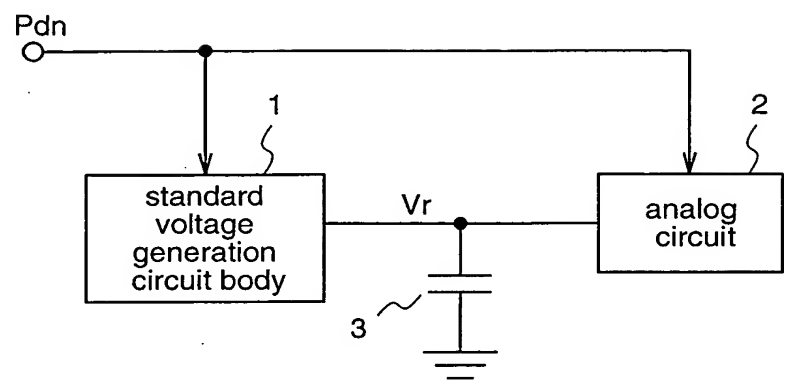


Fig.20 Prior Art

